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ABSTRACT OF THE DISCLOSURE

A liquid crystal display device includes dual bank type source driver PCBs installed at the top and the bottom of a liquid crystal panel, a gate driver PCB, and a staple-shaped main PCB formed in a body with a top portion and a bottom portion proceeding in the horizontal direction and a side portion proceeding in the vertical direction. The top portion and the bottom portion of the main PCB axially meet the side portion of the main PCB at a predetermined angle. A timing controller is mounted at the main PCB to process signals input from outside and generate driving signals. The main PCB transmits the relevant driving signals to the respective source driver PCBs and the gate driver PCB.